Amendments to the Claims:

Please amend claims 38 and 40, as noted in the listing below, and also add new claims 45-51 as indicated in the listing. This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

- 1. (Previously cancelled).
- 2. (Previously cancelled).
- 3. (Previously cancelled).
- 4. (Previously cancelled).
- 5. (Previously cancelled).
- 6. (Previously cancelled).
- 7. (Previously cancelled).
- 8. (Previously cancelled).
- 9. (Previously cancelled).
- 10. (Previously cancelled).
- 11. (Previously cancelled).
- 12. (Previously cancelled).
- 13. (Previously cancelled).
- 14. (Previously cancelled).
- 15. (Previously cancelled).
- 16. (Previously cancelled).

- 17. (Previously cancelled).
- 18. (Previously cancelled).
- 19. (Previously cancelled).
- 20. (Previously cancelled).
- 21. (Previously cancelled).
- 22. (Previously cancelled).
- 23. (Previously cancelled).
- 24. (Previously cancelled).
- 25. (Previously cancelled).
- 26. (Previously cancelled).
- 27. (Previously cancelled).
- 28. (Previously cancelled).
- 29. (Previously cancelled).
- 30. (Previously cancelled).
- 31. (Previously cancelled).
- 32. (Previously cancelled).
- 33. (Previously cancelled).
- 34. (Previously cancelled).
- 35. (Previously cancelled).
- 36. (Previously cancelled).
- 37. (Previously cancelled).
- 38. (Currently Amended) A method comprising:
 receiving a command to enter a fast program mode to program a first piece of data at

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a first address;

entering into said fast program mode;

programming said first piece of data at said first address in response to a <u>pulse of a</u> write signal;

checking whether termination of said fast program mode is indicated or if a second piece of data is to be written, wherein said checking further comprises detecting if an incoming address is different from said first address;

exiting said fast program mode if said termination of said fast program mode is indicated, else incrementing said first address to a second address; and

programming said second piece of data at said second address in response to another <u>pulse of said</u> write signal.

- 39. (Previously Added) The method of claim 38 further comprising receiving a confirmation command prior to entering said program mode.
- 40. (Currently amended) The method of claim 39 further comprising <u>automatically</u> issuing a status value to indicate a status for said programming of said first piece of data.
- 41. (Previously Added) The method of claim 40 wherein indication said termination comprises:

receiving an address unequal to said first address and a predefined data packet.

- 42. (Previously Added) The method of claim 41 wherein said predefined data packet is comprised of all 1's.
- 43. (Previously Added) The method of claim 41 wherein said predefined data packet is comprised of all 0's.

44. (New) The method of claim 38, wherein said first piece of data is a packet.
45. (New) The method of claim 38, wherein programming said first piece of data furthe comprises:
erasing the current contents of the first data address.
46. (New) The method of claim 38, wherein programming said second piece of data further comprises:
erasing the current contents of the second data address.
47. Cancel.
48. Cancel.
49. Cancel.
50. Cancel.
51. Cancel.